

Sheet 1 of 1Substituted Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-824001Application No.
10/629,325**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant
Larry R. Tate et al.Filing Date
July 28, 2003Group Art Unit
2631**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
JT	AA	4,785,394	11/1988	Fischer	 	 	
JT	AB	6,463,092	10/2002	Kim, et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
JT	AL	0 596 694	05/1994	Europe	 	 		
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
JT	AQ	Nogami, Kazutaka, et al. "Phase Modulation I/O Interface Circuit", IEEE International Solid-State Circuits Conference, New York, USA, vol. 37, pp. 108-109, 318, February (1994).
	AR	
	AS	
	AT	

Examiner Signature

/Julia Tu/

Date Considered

11/13/2006

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zalman (wtr) 08/11/2003.
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